

Electronic Acknowledgement Receipt

EFS ID:	2385080
Application Number:	10772293
International Application Number:	
Confirmation Number:	4918
Title of Invention:	ETCHANT FOR SIGNAL WIRE AND METHOD OF MANUFACTURING THIN FILM TRANSISTOR ARRAY PANEL USING ETCHANT
First Named Inventor/Applicant Name:	Hong-Sick Park
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Filer:	Frank Chau
Filer Authorized By:	
Attorney Docket Number:	8071-112T
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)
1	Request for Continued Examination (RCE)	8071-112T-RCE.pdf	74998	no	2
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Warnings:					
This is not a USPTO supplied RCE SB30 form.					
Information:					
2	Information Disclosure Statement (IDS) Filed	8071-112T-IDS.pdf	164634	no	4
			607ce65327ae80a9b22803b74847fe2166faedfe		
Warnings:					
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3	Foreign Reference	10-240150-ref.pdf	1840729	no	22
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Warnings:					
Information:					
4	Foreign Reference	10-282520-ref.pdf	905067	no	15
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Warnings:					
Information:					
5	Foreign Reference	1019990031518-ref.pdf	572409	no	11
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Warnings:					
Information:					
6	Foreign Reference	2000-275663-ref.pdf	715240	no	9
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Warnings:					
Information:					
7	Foreign Reference	2001-166336-ref.pdf	792044	no	11
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Warnings:					
Information:					

8	Fee Worksheet (PTO-06)	fee-info.pdf	8214	no	2
			a92e187c9c7fda463253f3cadf34186c78c1b7f1		
Warnings:					
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Total Files Size (in bytes):				5073335	
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